

SPECIFICATIONS

CIO-INT32

Interrupt Processor



**MEASUREMENT
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Revision 4, October, 2000

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Typical for 25°C unless otherwise specified.

Power consumption

+5V Operating 440 mA typical, 720 mA maximum

Counter/Timer and Parallel I/O

Device	Zilog Z85C36 (2)
Output High	2.4 volts min @ -250 μ A
Output Low	0.5 volts max @ +3.2 mA
Input High	2.0 volts min, 7 volts absolute maximum
Input Low	0.8 volts max, -0.3 volts absolute min
Power-up / reset state	Input mode (high impedance)
Configured as Digital Input/Output Ports:	
Digital Configuration	4 banks of 8 (Ports A and B), 2 banks of 4 (Port C), programmable by bit or bank as input or output
Number of channels	40 I/O
Configured as Counter/Timers: (Port A is a Digital I/O port only)	
Counter type	Zilog Z85C36
Counter Configuration	6, 16-bit counter/timers (Port B, High and Low nibble, and Port C) All Trigger (C1-C3 TRIG), Source (C1-C3 IN), Gate (C1-C3 GATE), and Output (C1-C3 OUT) available at user connector.
Clock input frequency	3 MHz maximum
High pulse width (clock input)	150 ns minimum
Low pulse width (clock input)	150 ns minimum
Trigger pulse width (high, low)	130 ns minimum

Interrupts:

The Z8536 is programmable to generate interrupts based on bit change, pattern recognition, level or edge triggered, whether configured as a digital I/O port or counter port. See Z8536 manual for further options.

Interrupts	Levels 2 through 7, jumper selectable (IRQ LEVEL)
Interrupt enable	Jumper-selectable (IRQ LEVEL) and external (INT ENABLE), active low (pulled high through resistor).
Interrupt sources	Jumper-selectable (INT SRC): 8536A interrupt output, 8536B interrupt output, 8536A OR'ed with 8536B, external (INT INPUT, positive edge-triggered) or None.
Interrupt output	Buffered output for each chip available at user connector (INTA OUT, INTB OUT).
Interrupt priority	Jumper-selectable (INT PRIORITY): No priority or 8536A interrupt has priority over 8536B interrupt.

Environmental

Operating temperature range	0 to 50°C
Storage temperature range	-20 to 70°C
Humidity	0 to 95%, non-condensing

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